13 ·

1. A diversity receiver system for receiving coded data modulated signal from a transmission channel, said transmission channel characterized by multiple transmission paths having variable transmission time and variable attenuation characteristics causing multiple copies of said coded data modulated signal, wherein said diversity receiver system comprises:

a signal acquisition device in communication with the transmission channel for reception of said multiple copies of said coded data modulated signal, for evaluation of signal characteristics of one or more copies of said multiple copies of said coded data modulated signal, for extraction coded data, control signals, and locking signals from the one or more copies of said multiple copies of said coded data;

a diversity circuit in communication with the signal acquisition device to receive said signal characteristics and said coded data, said control signals, and locking signals, said diversity circuit selecting from said signal characteristics, said control signals, and said locking signals, one of said copies of said coded data modulated signals; and

an error evaluation circuit in communication with the diversity circuit to receive the coded data from the selected copy of the coded data modulated signal, said error evaluation circuit evaluating said coded data signal for errors and providing an error signal to

| 23 | said diversity circuit indicating an error state of said selected  |
|----|--|
| 24 | data, wherein said diversity circuit selects a second copy of said |
| 25 | coded data modulated signal.                                       |

2. The diversity receiver system of claim 1 wherein the signal acquisition device comprises:

a plurality of receiving transducers in communication with said transmission channel, each transducer acquiring one of said copies of the coded data modulated signal from said transmission channel and converting said copy of the coded data modulated signal to a received electrical signal, said received electrical signal varying in magnitude dependant upon the transmission time and variable attenuation characteristics of said transmission channel; and

a plurality of receivers, each receiver in communication with one of said receiving transducers to amplify and condition said electrical signal and to extract said coded data, control signals, and locking signals from said received electrical signal.

3. The diversity receiver system of claim 2 wherein each of the plurality of the receiving transducers are assigned a selection priority such that the receiver in communication with a receiving transducer of a highest priority is selected by said diversity circuit.

3

4

5

6

- The diversity receiver system of claim 2 wherein if said error signal indicates said selected data is in error, the diversity circuit determines another receiver having a valid locking signal and transfers the data of said receiver to the error evaluation circuit.
- The diversity receiver system of claim 2 wherein if the error evaluation

  circuit indicates said selected data is in error but is correctable, said error

  evaluation circuit corrects said selected data.
- The diversity receiver system of claim 2 further comprising a data register in communication with said diversity circuit to retain said selected data and in communication with the error evaluation circuit so that said error evaluation circuit can retrieve said selected data.
- 7. The diversity receiver system of claim 2 further comprising a deinterleaving circuit in communication with the diversity circuit to organize
  said selected data such that said selected data is in a contiguous order
  prior to transfer to said error evaluation circuit.
- 1 8. The diversity receiver system of claim 1 wherein the signal acquisition device comprises:
  - a plurality of receiving transducers in communication with said
    transmission channel, each transducer acquiring one of said
    copies of the coded data modulated signal from said
    transmission channel and converting said copy of the coded

19

20

21

1

2

3

4

7 data modulated signal to a received electrical signal, said received electrical signal varying in magnitude dependant upon 8 the transmission time and variable attenuation characteristics of said transmission channel; 10 an transducer switch in communication with the plurality of 11 receiving transducers and in communication with the diversity 12 circuit, which upon reception of a transducer selection signal 13 from said diversity circuit selects one of said the electrical 14 signals of a selected receiving transducer; and 15 a receiver in communication with the transducer switch to amplify 16 and condition said electrical signal from selected receiving 17 transducer and to extract said coded data, control signals, and 18

transducer and to extract said coded data, control signals, and locking signals from said received electrical signal and in communication with the error evaluation circuit to transfer said coded data to the error evaluation circuit.

- 9. The diversity receiver system of claim 8 wherein each of the plurality of the receiving transducers are assigned a selection priority such that the receiving transducer of a highest priority is selected by said diversity circuit.
- 1 10. The diversity receiver system of claim 8 wherein if said error signal indicates said coded data received and extracted from the electrical signal

of the selected receiving transducer is in error, the diversity circuit

generates the transducer selection signal to select a second electrical

signal from a second receiving transducer to be transferred to the

receiver, said second electrical signal then having a valid locking signal

and transfers the data of said receiver to the error evaluation circuit.

- 1 11. The diversity receiver system of claim 8 wherein if the error evaluation

  circuit indicates said coded data received and extracted from the electrical

  signal of the selected receiving transducer is in error but is correctable,

  said error evaluation circuit corrects said coded data received and

  extracted from the electrical signal of the selected receiving transducer.
- 12. The diversity receiver system of claim 8 further comprising a data register
  in communication with said diversity circuit to retain said coded data
  received and extracted from the electrical signal of the selected receiving
  transducer and in communication with the error evaluation circuit so that
  said error evaluation circuit can retrieve said coded data.
- 1 13. The diversity receiver system of claim 7 further comprising a deinterleaving circuit in communication with the diversity circuit to organize
  said selected data such that said coded data received and extracted from
  the electrical signal of the selected receiving transducer is in a contiguous
  order prior to transfer to said error evaluation circuit.

| l  | 14. | A method for receiving coded data modulated signal from a transmission    |
|----|-----|---|
| 2  |     | channel, said transmission channel characterized by multiple transmission |
| 3  |     | paths having variable transmission time and variable attenuation          |
| 4  |     | characteristics causing multiple copies of said coded data modulated      |
| 5  |     | signal, said method for receiving the coded data modulated signal         |
| 6  |     | comprising the steps of:  |
| 7  |     | a) acquiring the multiple copies of coded data modulated signal;          |
| 8  |     | b) evaluating signal characteristics of one or more copies of said        |
| 9  |     | multiple copies of said coded data modulated signal;                      |
| 10 |     | c) extracting coded data, control signals, and locking signals from       |
| 11 |     | the one or more copies of said multiple copies of said coded              |
| 12 | •   | data;   |
| 13 |     | d) selecting one of said copies of said coded data modulated              |
| 14 |     | signals from said signal characteristics, said control signals,           |
| 15 |     | and said locking signals;   |
| 16 |     | e) performing an error check and correction upon said coded data;         |
| 17 |     | and   |
| 18 |     | f) if the error check and correction is not able to correct said coded    |
| 19 |     | data, repeating steps d) and e) until a data block is                     |
| 20 |     | successfully received.  |

- The method of claim 14 wherein the step of extracting the coded data includes the step of rearranging the coded data such that data within said coded data is in contiguous order.
- 1 16. The method of claim 14 wherein creating the locking signal comprises the steps of:
- detecting a synchronization signal within said coded data
   modulated signal;
- synchronizing said receiver to said coded data modulated signal;
  and
- detecting the control signals indicative of a beginning of said

  plurality of data blocks within said coded data modulated signal.
- The method of claim 14 wherein each copy of the multiple copies of the coded data modulated signal is assigned a selection priority such that selecting one of the multiple copies of the coded data modulated signals comprises the step of choosing the copy of the multiple copies of modulated coded data having a highest priority.
- 1 18. An apparatus for receiving coded data modulated signal from a
  2 transmission channel, said transmission channel characterized by multiple
  3 transmission paths having variable transmission time and variable
  4 attenuation characteristics causing multiple copies of said coded data

| 5  |     | modulated signal, said apparatus for receiving the coded data modulated  |
|----|-----|--|
| 6  |     | signal comprising the steps of:  |
| 7  |     | a) means for acquiring the multiple copies of coded data modulated       |
| 8  |     | signal;  |
| 9  |     | b) means for evaluating signal characteristics of one or more copies     |
| 10 |     | of said multiple copies of said coded data modulated signal;             |
| 11 |     | c) means for extracting coded data, control signals, and locking         |
| 12 |     | signals from the one or more copies of said multiple copies              |
| 13 |     | of said coded data;  |
| 14 |     | d) means for selecting one of said copies of said coded data             |
| 15 |     | modulated signals from said signal characteristics, said                 |
| 16 |     | control signals, and said locking signals;                               |
| 17 |     | e) means for performing an error check and correction upon said          |
| 18 |     | coded data; and  |
| 19 |     | f) means for repeatedly activating means d) and e) until a data          |
| 20 |     | block is successfully received, if the error check and                   |
| 21 |     | correction is not able to correct said coded data.                       |
| 1  | 19. | The apparatus of claim 18 wherein the means for extracting the coded     |
| 2  |     | data includes means for rearranging the coded data such that data within |
| 3  |     | said coded data is in contiguous order.                                  |

5

6

| 1  | 20. | The apparatus of claim 18 wherein means for creating the locking signal   |
|----|-----|---|
| 2  |     | comprises:  |
| 3  |     | means for detecting a synchronization signal within said coded data       |
| 4  |     | modulated signal;   |
| .5 |     | means for synchronizing said receiver to said coded data                  |
| 6  |     | modulated signal; and   |
| 7  |     | means for detecting the control signals indicative of a beginning of      |
| 8  |     | said plurality of data blocks within said coded data modulated            |
| 9  |     | signal.   |
| 1  | 21. | The apparatus of claim 18 wherein each copy of the multiple copies of the |
| 2  |     | coded data modulated signal is assigned a selection priority such that    |
| 3  |     | means for selecting one of the multiple copies of the coded data          |
| 4  |     | modulated signals comprises means for choosing the copy of the multiple   |
| 5  |     | copies of modulated coded data having a highest priority.                 |
| 1  | 22. | An apparatus for receiving coded data modulated signal from a             |
| 2  |     | transmission channel, said transmission channel characterized by multiple |
| 3  |     | transmission paths having variable transmission time and variable         |
| 4  |     | attenuation characteristics causing multiple copies of said coded data    |
|    |     |   |

coded data modulated signal comprising the steps of:

modulated signal, said apparatus executing a process for receiving the

| 7  |     | a) acquiring the multiple copies of coded data modulated signal;           |
|----|-----|--|
| 8  |     | b) evaluating signal characteristics of one or more copies of said         |
| 9  |     | multiple copies of said coded data modulated signal;                       |
| 10 |     | c) extracting coded data, control signals, and locking signals from        |
| 11 |     | the one or more copies of said multiple copies of said coded               |
| 12 |     | data;  |
|    |     |  |
| 13 |     | d) selecting one of said copies of said coded data modulated               |
| 14 |     | signals from said signal characteristics, said control signals,            |
| 15 |     | and said locking signals;  |
| 16 |     | e) performing an error check and correction upon said coded data;          |
| 17 |     | and  |
|    |     |  |
| 18 |     | f) if the error check and correction is not able to correct said coded     |
| 19 |     | data, repeating steps d) and e) until a data block is                      |
| 20 |     | successfully received.   |
| 1  | 23. | The apparatus of claim 22 wherein the step of extracting the coded data    |
| 2  |     | includes the step of rearranging the coded data such that data within said |
| 3  |     | coded data is in contiguous order.   |
| •  | 24  | The enperatus of claim 22 wherein creating the leading signal assessment   |
| 1  | 24. | The apparatus of claim 22 wherein creating the locking signal comprises    |
| 2  |     | the steps of:  |

| 3 |     | detecting a synchronization signal within said coded data                 |
|---|-----|---|
| 4 | ,   | modulated signal;   |
| 5 |     | synchronizing said receiver to said coded data modulated signal;          |
| 6 |     | and   |
| 7 |     | detecting the control signals indicative of a beginning of said           |
| 8 |     | plurality of data blocks within said coded data modulated signal.         |
| 1 | 25. | The apparatus of claim 22 wherein each copy of the multiple copies of the |
| 2 |     | coded data modulated signal is assigned a selection priority such that    |
| 3 |     | selecting one of the multiple copies of the coded data modulated signals  |
| 4 |     | comprises the step of choosing the copy of the multiple copies of         |
| 5 |     | modulated coded data having a highest priority.                           |
| 1 | 26. | A diversity receiver system for receiving a broadcast signal modulated    |
| 2 |     | with coded data, having multiple transmission paths, said diversity       |
| 3 |     | receiver comprising:  |
| 4 |     | a plurality of receiving transducers, each transducer acquiring said      |
| 5 |     | broadcast signal and converting said broadcast signal to a                |
| 6 |     | received electrical signal, said received electrical signal varying       |
| 7 |     | in magnitude dependant upon an intensity of said broadcast                |
| 8 |     | signal traversing said multiple transmission paths;                       |

a plurality of receivers, each receiver in communication with one of
said receiving transducers to amplify and condition said
electrical signal and to extract data and locking signals from
said received electrical signal;

a diversity circuit in communication with each of the plurality of receivers to receive said data and locking signals, said diversity circuit selecting one of the data signals from one of the plurality of receivers having a valid locking signal indicating said receiver is able to retrieve said coded data from the electrical signal; and an error evaluation circuit in communication with the diversity circuit to receive the selected data signal, said error evaluation circuit evaluating said selected data signal for errors and providing an error signal to said diversity circuit indicating an error state of

27. The diversity receiver system of claim 26 wherein if said error signal indicates said selected data is in error, the diversity circuit determines another receiver having a valid locking signal and transfers the data of said receiver to the error evaluation circuit.

said selected data.

The diversity receiver system of claim 26 wherein if the error evaluation circuit indicates said selected data is in error but is correctable, said error evaluation circuit corrects said selected data.

3

4

5

6

7

8

9

10

11

12

| l | 29. | The diversity receiver system of claim 126 further comprising a data          |
|---|-----|---|
| 2 |     | register in communication with said diversity circuit to retain said selected |
| 3 |     | data and in communication with the error evaluation circuit so that said      |
| 4 |     | error evaluation circuit can retrieve said selected data.                     |
|   |     |   |

- The diversity receiver system of claim 26 further comprising a deinterleaving circuit in communication with the diversity circuit to organize
  said selected data such that said selected data is in a contiguous order
  prior to transfer to said error evaluation circuit.
- 1 31. A method for receiving a broadcast signal modulated with coded data 2 comprising the steps of:
  - a) acquiring said broadcast signal at one of a plurality of receivers;
  - b) creating a locking signal indicating said receiver has successfully acquired said broadcast signal;
  - c) if said locking signal is able to be created, repeating steps a) and
     b) until a locking signal is created indicating successful
     acquisition of said broadcast signal;
  - d) extracting one data block of a plurality of data blocks of said coded data from said broadcast signal;
  - e) performing an error check and correction upon said data block for eliminating errors from said data block;

| 13 |     | f) if the error check and correction is not able to correct said data      |
|----|-----|--|
| 14 |     | block, repeating steps a) through e) until a data block is                 |
| 15 |     | successfully received; and   |
| 16 |     | g) repeatedly performing said error check and correction until all         |
| 17 |     | remaining data blocks of said broadcast signal are extracted               |
| 18 |     | and successfully checked and corrected for errors.                         |
| 1  | 32. | The method of claim 31 wherein the step of extracting the data block       |
| 2  |     | includes the step of rearranging the data block such that data within said |
| 3  |     | data block is in contiguous order.   |
| 1  | 33. | The method of claim 31 wherein creating the locking signal comprises the   |
| 2  |     | steps of:  |
| 3  |     | detecting a synchronization signal within said broadcast signal;           |
| 4  |     | synchronizing said receiver to said broadcast signal; and                  |
| 5  |     | detecting a start signal indicative of a beginning of said plurality of    |
| 6  |     | data blocks within said broadcast signal.                                  |
| 1  | 34. | An apparatus for receiving a broadcast signal modulated with coded data    |
| 2  |     | comprising:  |
| 3  |     | a) means for acquiring said broadcast signal at one of a plurality of      |
| 4  |     | receivers;   |
|    |     |  |

| 5  |             | b) means for creating a locking signal indicating said receiver has       |
|----|-------------|---|
| 6  |             | successfully acquired said broadcast signal;                              |
| 7  |             | c) means for activating the means of a) and b) until a locking signal     |
| 8  |             | is created indicating successful acquisition of said broadcast            |
| 9  |             | signal, if said locking signal is able to be created,;                    |
| 10 |             | d) means for extracting one data block of a plurality of data blocks      |
| 11 |             | from said broadcast signal;   |
| 12 |             | e) means for performing an error check and correction upon said           |
| 13 |             | data block for eliminating errors from said data block;                   |
| 14 |             | f) means for activating the means of a) through e) until a data block     |
| 15 |             | is successfully received, if the error check and correction is            |
| 16 |             | not able to correct said data block; and                                  |
| 17 |             | g) means for repeatedly performing said error check and correction        |
| 18 |             | until all remaining data blocks of said broadcast signal are              |
| 19 |             | extracted and successfully checked and corrected for errors.              |
| 1  | <b>3</b> 5. | The apparatus of claim 34 wherein the means for extracting the data block |
| 2  |             | includes means for rearranging the data block such that data within said  |
| 3  |             | data block is in contiguous order.  |
| 1  | 36.         | The apparatus of claim 34 wherein means for creating the locking signal   |
| 2  |             | comprises:  |

| 3  |     | means for detecting a synchronization signal within said broadcast          |
|----|-----|---|
| 4  |     | signal;   |
| 5  |     | means for synchronizing said receiver to said broadcast signal; and         |
| 6  |     | means for detecting a start signal indicative of a beginning of said        |
| 7  |     | plurality of data blocks within said broadcast signal.                      |
| 1  | 37. | An apparatus for acquiring coded data from a plurality of receivers, each   |
| 2  |     | receiver in communication with one of a plurality receiving transducers to  |
| 3  |     | amplify and condition an electrical signal acquired by said receiving       |
| 4  |     | transducers and extract said coded data and locking signals from said       |
| 5  |     | received electrical signal, each receiving transducer acquiring a broadcast |
| 6  |     | signal and converting said broadcast signal to said received electrical     |
| 7  |     | signal, said received electrical signal varying in magnitude dependant      |
| 8  |     | upon an intensity of said broadcast signal, said digital signal processing  |
| 9  |     | system executing a program comprising the steps of:                         |
| 10 |     | a) selecting one of said plurality of receivers;                            |
| 11 |     | b) detecting said locking signal indicating said receiver has               |
| 12 |     | successfully acquired said broadcast signal;                                |
| 13 |     | c) if said locking signal is not able to be created by said, repeating      |
| 14 |     | steps a) and b) until a locking signal is created indicating                |
| 15 |     | successful acquisition of said broadcast signal;                            |

| 16 |     | d) extracting one data block of a plurality of data blocks of said         |
|----|-----|--|
| 17 |     | coded data from said electrical signal;                                    |
| 18 |     | e) performing an error check and correction upon said data block           |
| 19 |     | for eliminating errors from said data block;                               |
| 20 |     | f) if the error check and correction is not able to correct said data      |
| 21 |     | block, repeating steps a) through e) until a data block is                 |
| 22 |     | successfully received; and   |
| 23 |     | g) repeatedly performing said error check and correction until all         |
| 24 |     | remaining data blocks of said electrical signal are extracted              |
| 25 |     | and successfully checked and corrected for errors.                         |
| 1  | 38. | The apparatus of claim 37 wherein the step of extracting the data block    |
| 2  |     | includes the step of rearranging the data block such that data within said |
| 3  |     | data block is in contiguous order.   |
| 1  | 39. | The apparatus of claim 37 wherein creating the locking signal comprises    |
| 2  |     | the steps of:  |
| 3  |     | detecting a synchronization signal within said broadcast signal;           |
| 4  |     | synchronizing said receiver to said broadcast signal; and                  |
| 5  |     | detecting a start signal indicative of a beginning of said plurality of    |
| 6  |     | data blocks within said broadcast signal.                                  |

40. A diversity receiver system for receiving coded data modulated signal from a transmission channel, said transmission channel characterized by multiple transmission paths having variable transmission time and variable attenuation characteristics causing multiple copies of said coded data modulated signal, wherein said diversity receiver system comprises:

a plurality of receiving transducers in communication with said transmission channel, each transducer acquiring one of said copies of the coded data modulated signal from said transmission channel and converting said copy of the coded data modulated signal to a received electrical signal, said received electrical signal varying in magnitude dependant upon the transmission time and variable attenuation characteristics of said transmission channel;

a transducer switch in communication with the plurality of receiving transducers, which upon reception of a transducer selection signal selects one of said the electrical signals of a selected receiving transducer;

a receiver in communication with the transducer switch to amplify and condition said electrical signal from a selected receiving transducer and to extract said coded data, control signals, and locking signals from said received electrical signal.

a diversity circuit in communication with the receiver to receive said signal characteristics and said coded data, said control signals, and locking signals, and in communication with the transducer switch, said diversity circuit selecting from said signal characteristics, said control signals, and said locking signals, generates the transducer selection signal designating one of said copies of said coded data modulated signals; and

an error evaluation circuit in communication with the receiver to receive the coded data from the selected copy of the coded data modulated signal, said error evaluation circuit evaluating said coded data signal for errors and providing an error signal to said diversity circuit indicating an error state of said selected data, wherein said diversity circuit generates a second transducer selection signal to select a second copy of said coded data modulated signal.

- The diversity receiver system of claim 40 wherein each of the plurality of the receiving transducers are assigned a selection priority such that the receiving transducer of a highest priority is selected by said diversity circuit.
- The diversity receiver system of claim 40 wherein if said error signal indicates said coded data received and extracted from the electrical signal of the selected receiving transducer is in error, the diversity circuit

- generates the transducer selection signal to select a second electrical
  signal from a second receiving transducer to be transferred to the
  receiver, said second electrical signal then having a valid locking signal
  and transfers the data of said receiver to the error evaluation circuit.
- The diversity receiver system of claim 40 wherein if the error evaluation

  circuit indicates said coded data received and extracted from the electrical

  signal of the selected receiving transducer is in error but is correctable,

  said error evaluation circuit corrects said coded data received and

  extracted from the electrical signal of the selected receiving transducer.
- The diversity receiver system of claim 40 further comprising a data
  register in communication with said diversity circuit to retain said coded
  data received and extracted from the electrical signal of the selected
  receiving transducer and in communication with the error evaluation circuit
  so that said error evaluation circuit can retrieve said coded data.
- The diversity receiver system of claim 40 further comprising a deinterleaving circuit in communication with the diversity circuit to organize
  said selected data such that said coded data received and extracted from
  the electrical signal of the selected receiving transducer is in a contiguous
  order prior to transfer to said error evaluation circuit.
- A method for receiving coded data modulated signal from a transmission channel, said transmission channel characterized by multiple transmission

| 3 | paths having variable transmission time and variable attenuation     |
|---|--|
| 4 | characteristics causing multiple copies of said coded data modulated |
| 5 | signal, said method for receiving the coded data modulated signal    |
| 6 | comprising the steps of:   |
| 7 | a) setting a priority value for each of a plurality of receiving     |

- a) setting a priority value for each of a plurality of receiving transducers, said plurality of receiving transducers in communication with the transmission channel such that said receiving transducers convert one of the copies of the coded data modulated signal to a received electrical signal;
- b) selecting one of the plurality of receiving transducers having a highest priority;
- c) evaluating signal characteristics of said received electrical signal
   from the one receiving transducer having the highest priority;
- d) extracting coded data, control signals, and locking signals from the received electrical signal from the one receiving transducer having the highest priority;
- e) performing an error check and correction upon said coded data;
- f) if the signal characteristics, control signals, locking signals, and results of said error check and correction indicate that the received electrical signal is not adequate for reconstruction

| 23 |             | of coded data from said received electrical signal, adjusting              |
|----|-------------|--|
| 24 |             | said priority value of said receiving transducer to a lower                |
| 25 |             | priority; and  |
| 26 |             | g) repeating steps d) through f) until said coded data is successfully     |
| 27 |             | received.  |
| 1  | <b>47</b> . | The method of claim 46 wherein the step of extracting the coded data       |
| 2  |             | includes the step of rearranging the coded data such that data within said |
| 3  |             | coded data is in contiguous order.   |
| 1  | 48.         | The method of claim 46 wherein extracting the control signals and the      |
| 2  |             | locking signal comprises the steps of:                                     |
| 3  |             | detecting a synchronization signal within said coded data                  |
| 4  |             | modulated signal;  |
| 5  |             | synchronizing said receiver to said coded data modulated signal;           |
| 6  |             | and  |
| 7  |             | detecting the control signals indicative of a beginning of said            |
| 8  |             | plurality of data blocks within said coded data modulated signal.          |
| 1  | 49.         | An apparatus for receiving coded data modulated signal from a              |
| 2  |             | transmission channel, said transmission channel characterized by multiple  |
| 3  |             | transmission paths having variable transmission time and variable          |
| 4  |             | attenuation characteristics causing multiple copies of said coded data     |

| 5  | modulated signal, said apparatus for receiving the coded data modulated |
|----|---|
| 6  | signal comprising:  |
| 7  | a) means for setting a priority value for each of a plurality of        |
| 8  | receiving transducers, said plurality of receiving transducers          |
| 9  | in communication with the transmission channel such that                |
| 10 | said receiving transducers convert one of the copies of the             |
| 11 | coded data modulated signal to a received electrical signal;            |
| 12 | b) means for selecting one of the plurality of receiving transducers    |
| 13 | having a highest priority;  |
| 14 | c) means for evaluating signal characteristics of said received         |
| 15 | electrical signal from the one receiving transducer having the          |
| 16 | highest priority;   |
| 17 | d) means for extracting coded data, control signals, and locking        |
| 18 | signals from the received electrical signal from the one                |
| 19 | receiving transducer having the highest priority;                       |
| 20 | e) means for performing an error check and correction upon said         |
| 21 | coded data;   |
| 22 | f) means for adjusting said priority value of said receiving            |
| 23 | transducer to a lower priority, if the signal characteristics,          |
| 24 | control signals, locking signals, and results of said error             |

| 25 |     | check and correction indicate that the received electrical                 |
|----|-----|--|
| 26 |     | signal is not adequate for reconstruction of coded data from               |
| 27 |     | said received electrical signal; and                                       |
| 28 |     | g) means for repeatedly activating means of d) through f) until said       |
| 29 |     | coded data is successfully received.                                       |
| 1  | 50. | The apparatus of claim 49 wherein the means for extracting the coded       |
| 2  |     | data includes the step of rearranging the coded data such that data within |
| 3  |     | said coded data is in contiguous order.                                    |
| 1  | 51. | The apparatus of claim 49 wherein means for extracting the control         |
| 2  |     | signals and the locking signal comprises the:                              |
| 3  |     | means for detecting a synchronization signal within said coded data        |
| 4  |     | modulated signal;  |
| 5  |     | means for synchronizing said receiver to said coded data                   |
| 6  |     | modulated signal; and  |
| 7  |     | means for detecting the control signals indicative of a beginning of       |
| 8  |     | said plurality of data blocks within said coded data modulated             |
| 9  |     | signal.  |
| 1  | 52. | An apparatus for receiving coded data modulated signal from a              |
| 2  |     | transmission channel, said transmission channel characterized by multiple  |
| 3  |     | transmission paths having variable transmission time and variable          |

attenuation characteristics causing multiple copies of said coded data

modulated signal, said apparatus for receiving the coded data modulated

signal executing a process comprising the steps of:

- a) setting a priority value for each of a plurality of receiving transducers, said plurality of receiving transducers in communication with the transmission channel such that said receiving transducers convert one of the copies of the coded data modulated signal to a received electrical signal;
- b) selecting one of the plurality of receiving transducers having a highest priority;
- c) evaluating signal characteristics of said received electrical signal from the one receiving transducer having the highest priority;
- d) extracting coded data, control signals, and locking signals from the received electrical signal from the one receiving transducer having the highest priority;
- e) performing an error check and correction upon said coded data;
- f) if the signal characteristics, control signals, locking signals, and results of said error check and correction indicate that the received electrical signal is not adequate for reconstruction of coded data from said received electrical signal, adjusting

| 24  |     | said priority value of said receiving transducer to a lower                |
|-----|-----|--|
| 25  |     | priority; and  |
| 26  |     | g) repeating steps d) through f) until said coded data is successfully     |
| 27  |     | received.  |
| 1   | 53. | The apparatus of claim 52 wherein the step of extracting the coded data    |
| 2   |     | includes the step of rearranging the coded data such that data within said |
| . 3 | •   | coded data is in contiguous order.   |
| 1   | 54. | The apparatus of claim 52 wherein extracting the control signals and the   |
| 2   |     | locking signal comprises the steps of:                                     |
| 3   |     | detecting a synchronization signal within said coded data                  |
| 4   |     | modulated signal;  |
| 5   | ·   | synchronizing said receiver to said coded data modulated signal;           |
| 6   |     | and  |
| 7   |     | detecting the control signals indicative of a beginning of said            |
| 8   |     | plurality of data blocks within said coded data modulated signal.          |